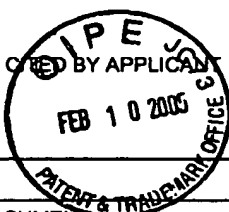


Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 219713US2		SERIAL NO. 10/075,464	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Shigeru KAWANAKA			
				FILING DATE February 15, 2002		GROUP 2826	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
<i>W</i>	AO	9-135030	05/20/1997	JAPAN (with English Abstract)		X	
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
	AW						
	AX						
	AY						
	AZ						<input type="checkbox"/> Additional References sheet(s) attached
Examiner <i>[Signature]</i>					Date Considered <i>5/15/08</i>		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 219713US2		SERIAL NO. 10/075,464	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Shigeru KAWANAKA			
FILING DATE February 15, 2002				GROUP 2826			



U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
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FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION		
					YES	NO	
<i>M</i>	AO	110488	09/18/1996	KOREA (corresponding to EP 0 551 214 A2)			X
<i>M</i>	AP	0 551 214 A2	07/14/1993	EUROPE			
<i>M</i>	AQ	5-110037	04/30/1993	JAPAN (with English Abstract)			X
<i>M</i>	AR	WO 95/15562	06/08/1995	WIPO			
	AS						
	AT						
	AU						
	AV						

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)		
<i>M</i>	AW	Hideto HIDAHA, et al., "A High-Density Dual-Port Memory Cell Operation and Array Architecture for ULSI DRAM's", IEEE Journal of Solid-State Circuits, vol. 27, no. 4, April 1992, pages 610-617
	AX	
	AY	
	AZ	

☐ Additional References sheet(s) attached

Examiner <i>[Signature]</i> 3/13/02	Date Considered
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